

TE 4916

U.S. PTO  
09/994279



438	Class	Subclass	ISSUE CLASSIFICATION
905			

PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED	O.I.P.E.	PATENT DATE
	<i>MA G.O.</i>	

APPLICATION NO. 09/994279	CONT/PRIOR D	CLASS 438	SUBCLASS 905	ART UNIT .2812	EXAMINER
------------------------------	-----------------	--------------	-----------------	-------------------	----------

APPLICANTS

Tony Chisna  
Karl Lerner

TITLE

Method for integrated circuitry including and subsequent atomic layer deposition within a single processing chamber

PTO-2040  
12/99

Please  
Don't remove this coverage -  
thanks -

_____ Date	_____ Date Paid
ISSUE BATCH NUMBER	

prohibited by the United States Code Title 35, Sections 122, 181 and 368.  
employees and contractors only.

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM  
(Attached in pocket on right inside flap)